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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/790,184	03/02/2004	Takashi Oshima	520.43569X00	2953	
20457	7590 11/29/2005		EXAM	EXAMINER	
	LI, TERRY, STOUT &	SHINGLETON	SHINGLETON, MICHAEL B		
SUITE 1800	1300 NORTH SEVENTEENTH STREET SUITE 1800		ART UNIT	PAPER NUMBER	
ARLINGTO	N, VA 22209-3873		2817		

DATE MAILED: 11/29/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)		
Sypplemental Notice of Allowability	10/790,184	OSHIMA ET AL.		
Notice of Allowability	Examiner	Art Unit		
	Shingleton	2817	·	
The MAILING DATE of this communication appeal claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	ears on the cover sheet with the co (OR REMAINS) CLOSED in this applior other appropriate communication GHTS. This application is subject to	plication. If not include will be mailed in due	ed course. <b>THIS</b>	
1. ⊠ This communication is responsive to <u>supplemental to the N</u>	IOA mailed 8-24-05.			
2. ⊠ The allowed claim(s) is/are <u>1-10</u> .				
<ol> <li>Acknowledgment is made of a claim for foreign priority una)</li></ol>	been received. been received in Application No		ion from the	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	IENT of this application.			
<ol> <li>A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give</li> </ol>	itted. Note the attached EXAMINER es reason(s) why the oath or declara	'S AMENDMENT or Nation is deficient.	OTICE OF	
<ul> <li>5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.</li> <li>(a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached</li> <li>1) hereto or 2) to Paper No./Mail Date</li> <li>(b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date</li> <li>Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).</li> </ul>				
<ol> <li>DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT</li> </ol>	SIT OF BIOLOGICAL MATERIAL F FOR THE DEPOSIT OF BIOLOGIC	nust be submitted. r AL MATERIAL.	note the	
Attachment(s)  1. Notice of References Cited (PTO-892)  2. Notice of Draftperson's Patent Drawing Review (PTO-948)  3. Information Disclosure Statements (PTO-1449 or PTO/SB/C Paper No./Mail Date	8. Examiner's Statements 9. Other  Sojoutpag Robert Pascal Anosialeding Supervisory Patent Exa	(PTO-413), te ment/Comment ent of Reasons for Allo		

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Application/Control Number: 10/790,184

Art Unit: 2817

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Claim 1, line 9 as presented in the preamendment filed March 2, 2004 has been amended to insert --directly-- between "control" and "received".

Authorization for this examiner's amendment was given in a telephone interview with Paul Skwierawski on November 21, 2005.

Examinois anodonest

OSHIMA et al., Serial No. Unassigned Prelim, Amdt. filed 03 March 2004

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## IN THE CLAIMS:

- 1. (Original) A variable gain amplifier circuit comprising:
- a plurality of variable gain circuits;

a plurality of level detector circuits which detect signal levels at respective inputs of said plurality of variable gain circuits; and

a plurality of gain control circuits which separately control respective gains of said plurality of variable gain circuits,

wherein each of said plurality of gain control circuits determines a gain to be set therein, based on the signal level detected by one which is connected thereto of said plurality of level detector circuits and information about gain control received from a part or all of other gain control circuits existing in its preceding stage or stages.

2. (Original) The variable gain amplifier circuit according to claim 1, wherein said plurality of level detector circuits are connected to said plurality of variable gain circuits, respectively, and

wherein said plurality of gain control circuits are connected to said plurality of level detector circuits, respectively.

3. (Original) The variable gain amplifier circuit according to claim 1, further comprising:

a plurality of filters separately connected to respective outputs of said plurality of variable gain amplifiers; and